

Fig. 1

FIG. 2 is a schematic diagram of a system 100 for providing a user with a set of data. The system 100 includes a processor 110, a memory 120, and a user interface 130. The processor 110 is configured to receive a request from the user interface 130 and to access the memory 120 to retrieve data in response to the request. The memory 120 is configured to store the data. The user interface 130 is configured to provide the data to the user.

R12	B1	1	B2	2	5	6	9	10
R13	B3	3	B4	4	7	8	11	12
R34		13		14	17	18	21	22
		15		16	19	20	23	24

Fig. 2

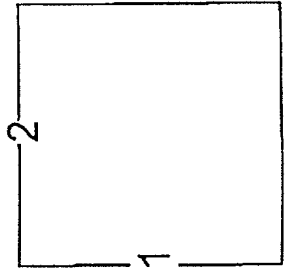


Fig. 3

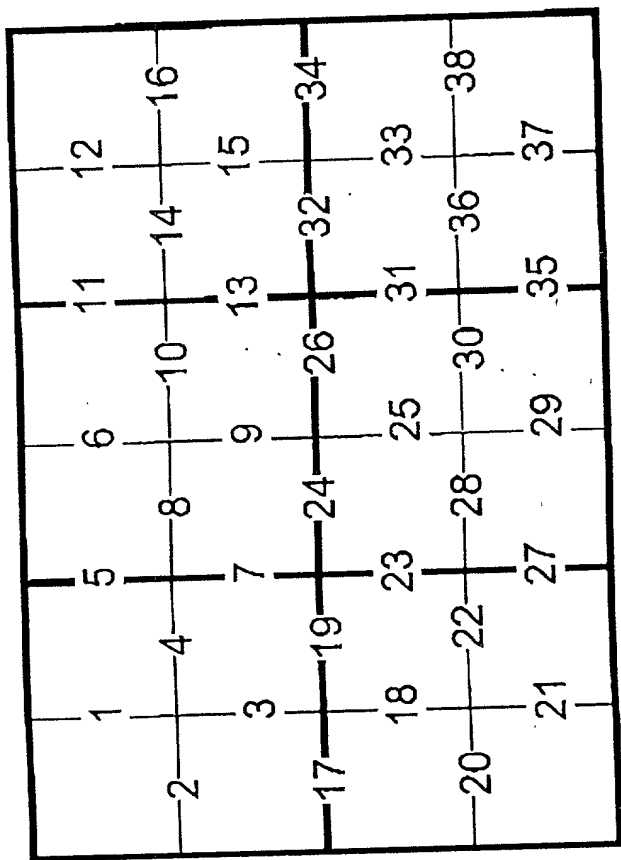


Fig. 4

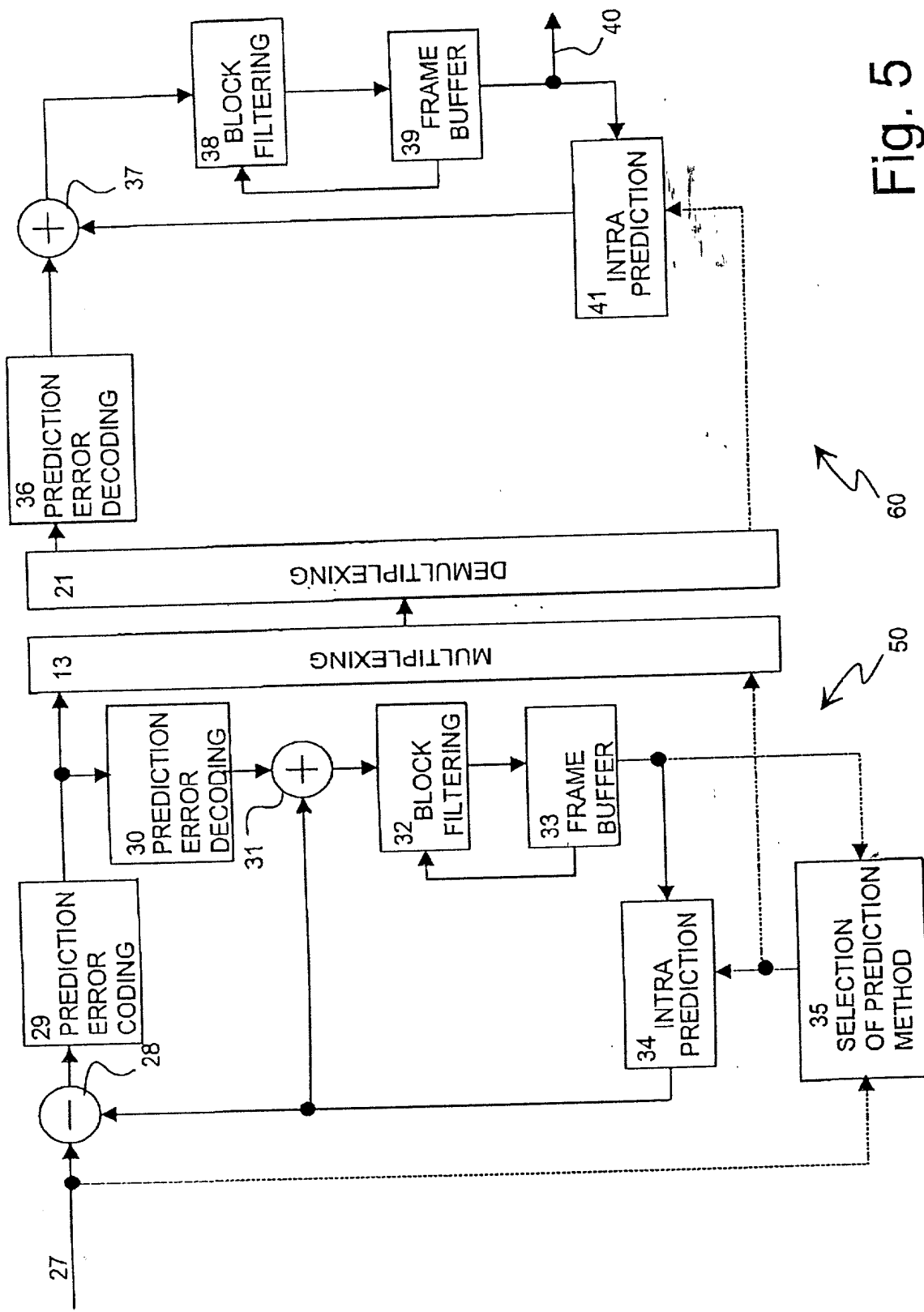


Fig. 5

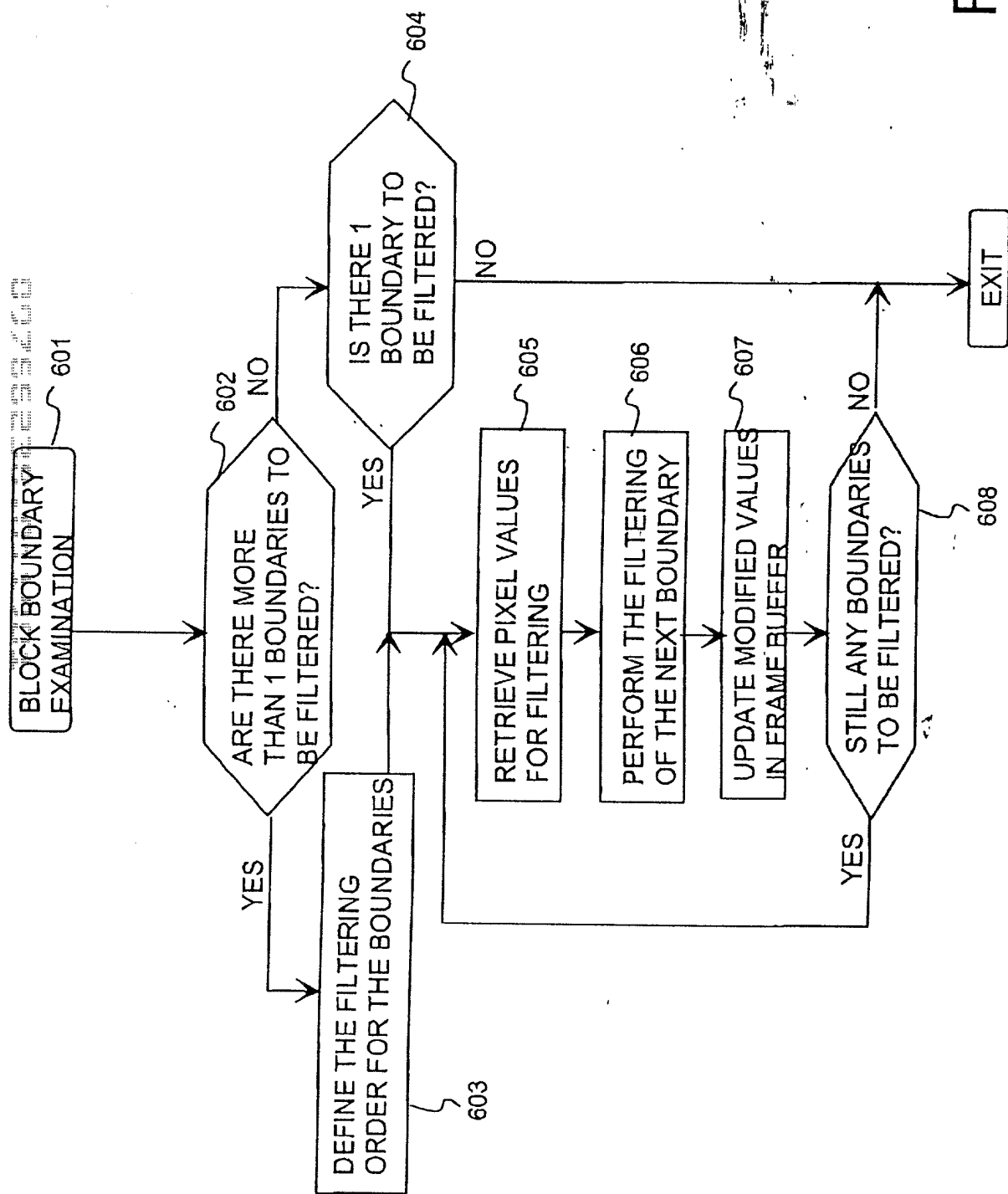


Fig. 6

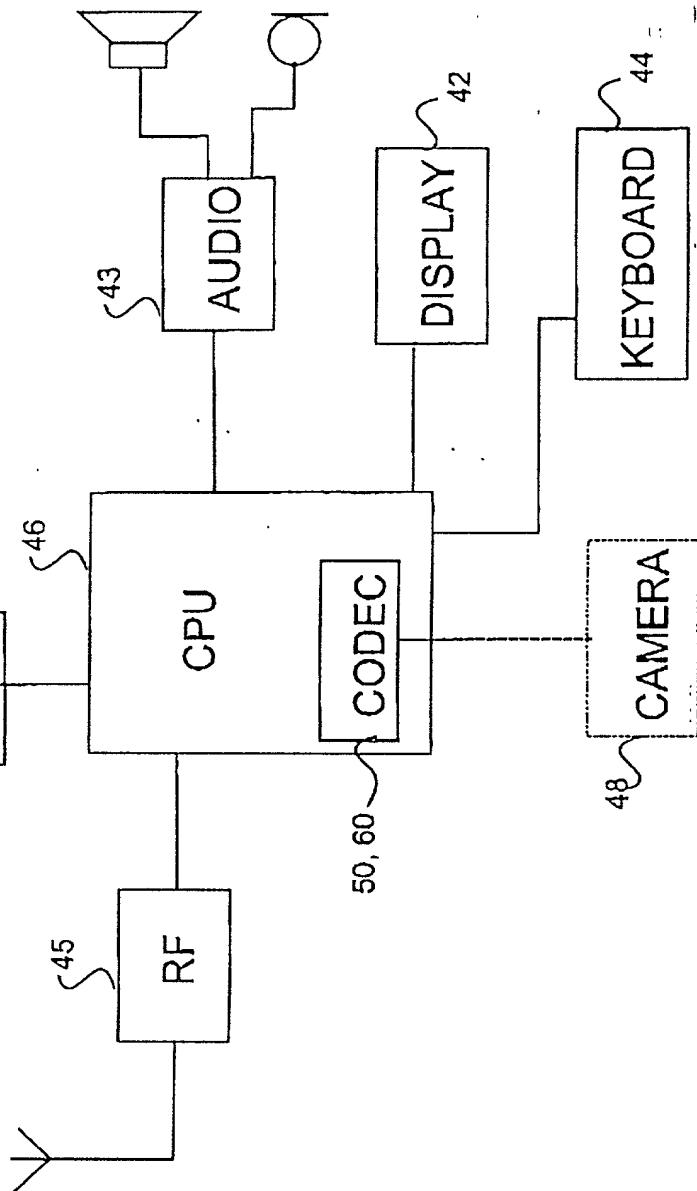


Fig. 7